

CURRICULUM VITAE

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Prasenjit Saha

Personal Profile

Name : Prasenjit Saha
Date of Birth : 29th January 1991.
Nationality : Indian.
Present Address : Ananda Nagar, Adabari tiniali, P.O. Pandu, Guwahati-781012, Assam.

Education

M.Tech in Signal Processing and VLSI Design, Gauhati University, Guwahati, Assam, India.
B.E in Electronics and Telecommunication Engineering, Assam Engineering College, Guwahati, Assam.

Professional Experience

Presently working as Guest Faculty in Assam Engineering College, Guwahati.

Research Interest

1. Device Modeling.
 2. VLSI Design.
 3. Programming Languages.
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List of Publication (in 2018)

Book Chapter:

1. "Implementation of a Doped-Pocket Region in order to enhance the Device Performance of a MOSFET", Lecture Notes in Electrical Engineering, Springer, (to be published).

International Conference:

1. "Design and Simulation of a Modified MOSFET having a Doped-Pocket Region in the Substrate", ICRIEECE 2018, IEEE Conference.
 2. "Simulation Modeling and Study on the Impacts of Substrate Concentration and Gate Work Function in MOSFET having doped-pocket Substrate", GUCON 2018, IEEE Conference.
 3. "Design and Study On Various Impacts of Device parameters in MOSFET having Doped-Pocket Substrate", ICICET 2018, IEEE Conference.
 4. "Three Dimensional Design and Implementation of Doped-Pocket Substrate in N-MOSFET", ICSCAN 2018, IEEE Conference.
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Membership

IEEE Member: Membership no. 94585433

Projects

1. Design of a 16 bit Microprocessor using VHDL.
 2. Design of Advanced Encryption System (AES) using VHDL.
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Technical Skills

Programming in *Silvaco*, VHDL, Verilog HDL, Assembly languages-8085, 8051, 8086, Analog Design in Cadence, Latex, MATLAB, Basics of C Programming, C++, Data structures.
